

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Fried *et al.*

Art Unit:

Serial No.:

Dkt. No.: BUR920010162US2

Filed:

Examiner:

Title: **DUAL DOUBLE GATE TRANSISTOR AND METHOD FOR FORMING**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Preliminary Amendment**

Sir:

Kindly enter this amendment prior to initial examination

**In the Specification:**

Page 1, between lines 1 and 2, insert: --This application is a divisional of Serial No.  
10/063,323; filed on 4/11/2002.--